



PTO/SB/08B (08-03)

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Substitute for form 1449/PTO

Complete if Known**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

2

Application Number	09/865,999
Filing Date	May 25, 2001
First Named Inventor	Guillermo ALVAREZ
Art Unit	2123
Examiner Name	William D. THOMSON

Attorney Docket Number

10003525-1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
WR	1A	Guillermo A. ALVAREZ et al., Declustered Disk Array Architectures with Optimal and Near-optimal Parallelism, Proc. of the 25th Annual ACM/IEEE Symposium on Computer Architecture, 1998, pp. 109-120, IEEE Computer Society, Washington DC.	
	1B	Guillermo A. ALVAREZ et al., Efficient verification of performability guarantees, Fifth International Workshop on Performability Modeling of Computer and Communication Systems, September 2001, Erlangen, Germany, .	
	1C	Scott A. BARNETT et al., Performability of disk-array-based video servers, Multimedia Systems, 1998, 6:60-74, Springer-Verlag, Berlin, Germany.	
	1D	Ing-Ray CHEN, Effect of Probabilistic Error Checking Procedures on Performability of Robust Objects, Proceedings of the 1993 ACM/SIGAPP Symposium on Applied Computing: States of the Art and Practice, 1993, pp. 677 - 681, ACM Press, New York, NY.	
	1E	S. M. Rezaul ISLAM, Performability Analysis of Disk Arrays, Proceedings of the 36th Midwest Symposium on Circuits and Systems, 1993, IEEE, New York, NY.	
	1F	Edward K. LEE et al., An Analytic Performance Model of Disk Arrays, Proceedings of the 1993 ACM SIGMETRICS Conference on Measurement and Modeling of Computer Systems, 1993, pp. 98 - 109, ACM Press, New York, NY.	
	1G	Arif MERCHANT et al., Analytic Modeling of Clustered RAID with Mapping Based on Nearly Random Permutation, IEEE Transactions on Computers, 1996, Vol. 45, No. 3, pp. 367-373 , IEEE Computer Society, Washington DC.	
	1H	Arif MERCHANT et al., Disk Array Models in Minerva, HP Labs Technical Report, HPL-2001-118, May 15, 2001, Hewlett-Packard, Palo-Alto, CA.	
	1I	David A. PATTERSON et al., A Case for Redundant Arrays of Inexpensive Disks (RAID), Proceedings of the 1988 ACM SIGMOD International Conference on Management of Data, 1988, pp. 109-116, ACM Press, New York, NY.	
WR	1J	Alexander THOMASIAN et al., Performance Analysis of RAID5 Disk Arrays with a Vacationing Server Model for Rebuild Mode Operation, Proceedings of the Tenth International Conference on Data Engineering, 1994, pp. 111 - 119, IEEE Computer Society, Washington DC.	

Examiner Signature		Date Considered	4-1-05
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